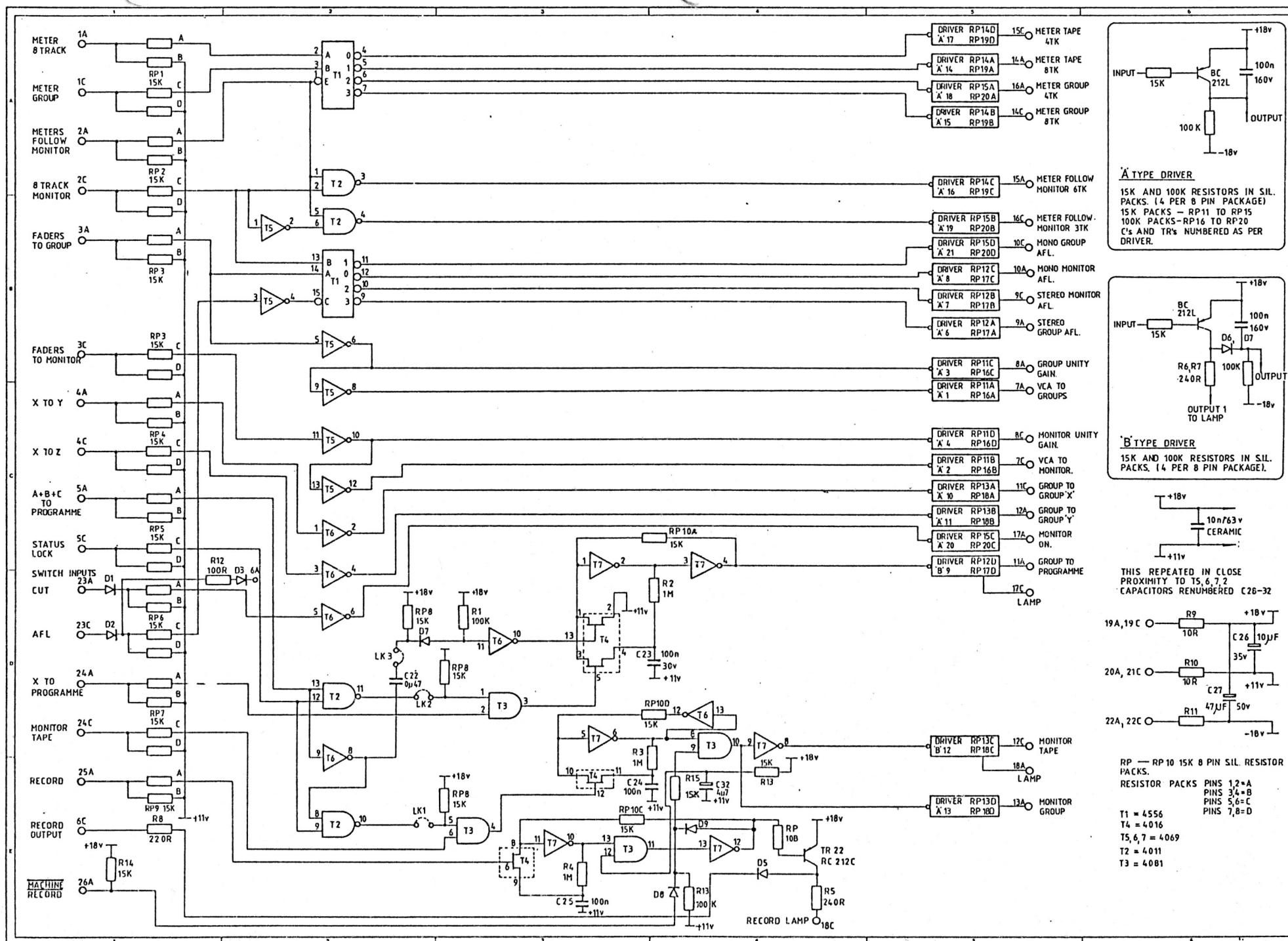


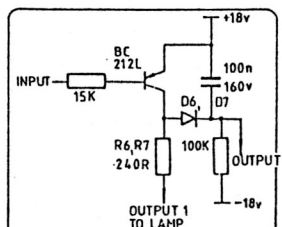


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Rev	Issue	Chkd	Details
0	a		PROTOTYPE
0	b		
0	c		
1	a		REDRAWN 8.84
2	a	10.84	REHEARSE FACIL ADDED
2	b	Oct 85	CONNECTOR ADDR ADDED
3	a	DM Nov 85	RP130 CON. FROM TO T3,10
3	b	OTC Jan 86	X TO E AMENDED TO X TO Z
3	c	OTC Apr 86	T6 PIN 7 AMENDED PIN 9
3	d	8.8.86 Aug 86	DRIVER 'A' & DRIVER 'B' ADDED

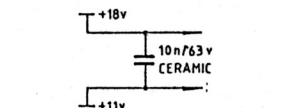
'A' TYPE DRIVER

15K AND 100K RESISTORS IN SIL PACKS. (4 PER 8 PIN PACKAGE).
15K PACKS - RP11 TO RP15
100K PACKS - RP16 TO RP20
C's AND TR's NUMBERED AS PER DRIVER.

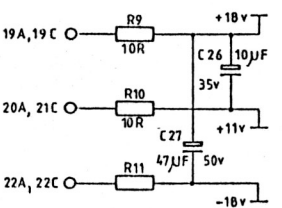


'B' TYPE DRIVER

15K AND 100K RESISTORS IN SIL PACKS. (4 PER 8 PIN PACKAGE).



THIS REPEATED IN CLOSE PROXIMITY TO T5, 6, 7, 2
CAPACITORS RENUMBERED C26-32



RP - RP10 15K 8 PIN SIL. RESISTOR PACKS.
RESISTOR PACKS PINS 1,2=A
PINS 3,4=B
PINS 5,6=C
PINS 7,8=D

T1 = 4556
T4 = 4016
T5, 6, 7 = 4069
T2 = 4011
T3 = 4081

Title
688v GROUP LOGIC

Dwg. No. T82158-70

Solid State Log